

In Place of FORM PTO-1449 (Modified)

Serial No. \_\_\_\_\_

**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT**

Applicant: Accapadi, et. al.

Filing Date: \_\_\_\_\_

Group: \_\_\_\_\_

Atty. Docket No. AUS920030414US1

**U. S. PATENT DOCUMENTS**

<u>Examiner Initial</u>	<u>Document Number</u>	<u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Subclass</u>	<u>Filing Date if Appropriate</u>
_____	AA					
_____	AB					
_____	AC					
_____	AD					
_____	AE					
_____	AF					
_____	AG					
_____	AH					
_____	AI					
_____	AJ					
_____	AK					
_____	AL					

**FOREIGN PATENT DOCUMENTS**

<u>Examiner Initial</u>	<u>Document Number</u>	<u>Date</u>	<u>Country</u>	<u>Class</u>	<u>Subclass</u>	<u>Translation Yes No</u>
_____	AM					
_____	AN					
_____	AO					
_____	AP					

Examiner  
Initial

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

<u>/C.Q./</u>	AQ	Luo et al., "Balancing Throughput and Fairness in SMT Processors," IEEE, 2001, p. 164-171.
_____	AR	
_____	AS	
_____	AT	
_____	AU	

Examiner:

/Camquy Truong/

Date Considered:

06/19/2007

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.